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| INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(Not for submission under 37 CFR 1.99)</i> | Application Number | | 10001477 |
| | Filing Date | | 2001-11-01 |
| | First Named Inventor | | Steve Roe |
| | Art Unit | | 2123 |
| | Examiner Name | | Jason Scott Proctor |
| | Attorney Docket Number | | CD01203M |

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|---|---|--------------------------|
| 1 | Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al., filed on 09/26/2001; 25 pages | <input type="checkbox"/> |
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| 2 | USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 05/28/2003; 6 pages | <input type="checkbox"/> |
| 3 | Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block," Monte Mar, filed on 08/29/2001; 46 pages | <input type="checkbox"/> |
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| 4 | USPTO Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller," Kutz et al., filed on 08/03/2001; 38 pages | <input type="checkbox"/> |
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| 9 | USPTO Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot," Nemecek et al., filed on 11/01/2001; 44 pages | <input type="checkbox"/> |
| 10 | USPTO Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer," Nemecek et al., filed on 11/01/2001; 47 pages | <input type="checkbox"/> |
| 11 | USPTO Application Number 10/002,217 (CD01207M): "Conditional Branching in an In-Circuit Emulation System," Craig Nemecek, filed on 11/01/2001; 43 pages | <input type="checkbox"/> |

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| 12 | USPTO Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer," Nemecek et al., filed on 11/14/2001; 46 pages | <input type="checkbox"/> |
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| 21 | The Provisional Application 60/243,708 (CD00167P) "Advanced Programmable Microcontroller Device"; Snyder et al., filed on 10/26/2000; 277 pages | <input type="checkbox"/> |
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| 25 | USPTO Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies," Suliam et al., filed on 10/25/2001; 49 pages | <input type="checkbox"/> |
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| | 2 | Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 259 pages | <input type="checkbox"/> |
| | 3 | USPTO Application Number 09/975,105 (CD00184): "Host to FPGA Interface in an In-Circuit Emulation System," Craig Nemecek, filed on 10/10/2001; 44 pages | <input type="checkbox"/> |

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| | Art Unit | 2123 |
| | Examiner Name | Jason Scott Proctor |
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| 11 | Efstathiou, "Analog Electronics: Basic Circuits of Operational Amplifiers," < http://web.archive.org/web/20021231045232/ > December 31, 2002, version, retrieved from the Internet Archives; 10 pages | <input type="checkbox"/> |

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| | Filing Date | 2001-11-01 |
| | First Named Inventor | Steve Roe |
| | Art Unit | 2123 |
| | Examiner Name | Jason Scott Proctor |
| | Attorney Docket Number | CD01203M |

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